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1	fpga same bus\$4 same switch\$4
2	S1 and emulat\$4 and channel
3	S2 and (pcb pwb circuit adj (board card))
4	S2 and (pcb pwb circuit adj (board card)) and "716"/\$.ccls.
5	sample and fpga and emulat\$4
6	sample and fpga and emulat\$4and "71"/\$.ccls.
7	sample and fpga and emulat\$4and "716"/\$.ccls.
8	sample and fpga and emulat\$4 and "716"/\$.ccls.
9	"6377912".pn. and bus same (switch\$4)
10	"6377912".pn. and bus\$4 same (switch\$4)
11	"5737235".pn. and bus\$4 same (switch\$4)
12	"5737235".pn. and bus\$4 same (switch\$4) and fpga
13	programmable with interconnect same switch\$4 and emulat\$4 and fpga and "716"/\$.ccls.
14	programmable with interconnect same switch\$4 and emulat\$4 and fpga and ("716"/\$.ccls. or "326"/\$.ccls.)
15	programmable with interconnect\$5 same switch\$4 and emulat\$4 and fpga and ("716"/\$.ccls. or "326"/\$.ccls.) and bus\$4 same (programmable with interconnect\$6 or switch\$4)
16	programmable with interconnect\$5 same switch\$4 and emulat\$4 and fpga and ("716"/\$.ccls. or "326"/\$.ccls.) and bus\$4 same (programmable with interconnect\$6 or switch\$4.) and @ad<"20030826"
17	"6377912".pn. and bus\$4 and switch\$4
18	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5
19	S24 and "716"/\$.ccls. and @ad<"20030826"
20	fpga same bus\$4 and channel same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5
21	fpga same bus\$4 and channel same (connect\$5 interconnect\$5) same switch\$5 same bus\$4 and emulat\$5 and "716"/\$.ccls. and @ad<"20030826"
22	fpga and channel same (connect\$5 interconnect\$5) same switch\$5 same bus\$4 and emulat\$5 and "716"/\$.ccls. and @ad<"20030826" and (pcb pwb circuit adj (board card))
23	programmable with interconnect\$5 same switch\$4 and emulat\$4 and fpga and ("716"/\$.ccls. or "326"/\$.ccls.) and bus\$4 same (programmable with interconnect\$6 or switch\$4) and @ad<"20030826" and bus\$4 same channel
24	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5 and @ad<"20030730" and bus\$5 same channel
25	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5 and @ad<"20030730" and bus\$5 same channel and programmable with interconnect\$4 and programmable with switch\$4
26	"6377912".pn. and fpga
27	"6377912".pn. and bus\$5
28	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5 and @ad<"20030730" and bus\$5 same channel and bus\$4 same programmable (switch\$4 interconnect\$4) and programmable with switch\$4

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29	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5 and @ad<"20030730" and bus\$5 same channel and bus\$4 same programmable same (switch\$4 interconnect\$4) and programmable with switch\$4
30	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5 and @ad<"20030730" and bus\$5 same channel and bus\$4 same programmable same (switch\$4 interconnect\$4) same switch\$4
31	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 and emulat\$5 and @ad<"20030730" and bus\$5 same channel and bus\$4 same programmable same (switch\$4) same interconnect\$4
32	fpga same bus\$4 and alternat\$6 same (connect\$5 interconnect\$5) same switch\$5 same bus\$5 and emulat\$5 and @ad<"20030730" and bus\$4 same programmable same (switch\$4) same interconnect\$4
33	"5742181".pn. and fpga and bus\$4
34	"5742181".pn. and fpga and bus\$4 and (switch\$4 multipl mux)
35	"6377912".pn. and emulat\$5
36	"6377912".pn. and (fpga)
37	"6377912".pn. and (bus\$5)
38	"6377912".pn. and logic adj chip same fpga
39	"6377912".pn. bus\$4 same (switch\$4 cross\$8 bar multiplex\$5)
40	"6377912".pn. and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5)
41	("6163168" "5742181" "5737235" "6181163") "pn." and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5)
42	("6163168" "5742181" "5737235" "6181163") .pn. and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5)
43	("6163168" "5742181" "5737235" "6181163") .pn. and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field)
44	("6163168" "5742181" "5737235" "6181163") .pn. and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) and emulat\$5
45	("6389379") .pn. and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) and emulat\$5
46	("6163168" "5742181" "5737235" "6181163") .pn. and bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) same (connect\$6 interconnect\$4 coupl\$5) and emulat\$5
47	free\$4 same bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) same (connect\$6 interconnect\$4 coupl\$5) and emulat\$5
48	free\$4 same bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) same (connect\$6 interconnect\$4 coupl\$5) and emulat\$5 and @ad<"20030826"
49	alternat\$4 same bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) same (connect\$6 interconnect\$4 coupl\$5) and emulat\$5 and @ad<"20030826"
50	(alternat\$4 near4 (connect\$6 interconnect\$4 coupl\$5)) same bus\$4 same (switch\$4 cross\$8 bar multiplex\$5) same (fpga field) and emulat\$5 and @ad<"20030826"
51	(alternat\$4 near4 (connect\$6 interconnect\$4 coupl\$5)) same bus\$4 same (switch\$4 cross\$8 bar multiplex\$5 programmable) same (fpga field) and emulat\$5 and @ad<"20030826"
52	"5742181".pn. and fpga
53	"5742181".pn. and emulat\$6

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54	"5742181".pn. and bus\$4
55	"5742181".pn. and bus\$4 same fpga
56	"5742181".pn. and bus\$4 same (field fpga)
57	"5742181".pn. and bus\$4 same (switch\$4 matri\$4)
58	"5742181".pn. and bus\$4 same (switch\$4 matri\$4 mulriplex\$4)
59	((switchabl\$5 interchang\$6) near4 (connect\$6 interconnect\$4 coupl\$5)) same bus\$4 same (switch\$4 cross\$8 bar multiplex\$5 programmable) same (fpga field) and emulat\$5 and @ad<"20030826"
60	"20050028127" and board same (upper lower plug\$4)
61	emulat\$4 and "703"/\$.ccls. and fpga and bus\$4
62	emulat\$4 and "703"/\$.ccls. and fpga same bus\$4 and @ad<"20030826" and bus\$4 same (switch\$4 cross\$8 bar programmable interchang\$6) same (connect\$7 interconnect\$4)